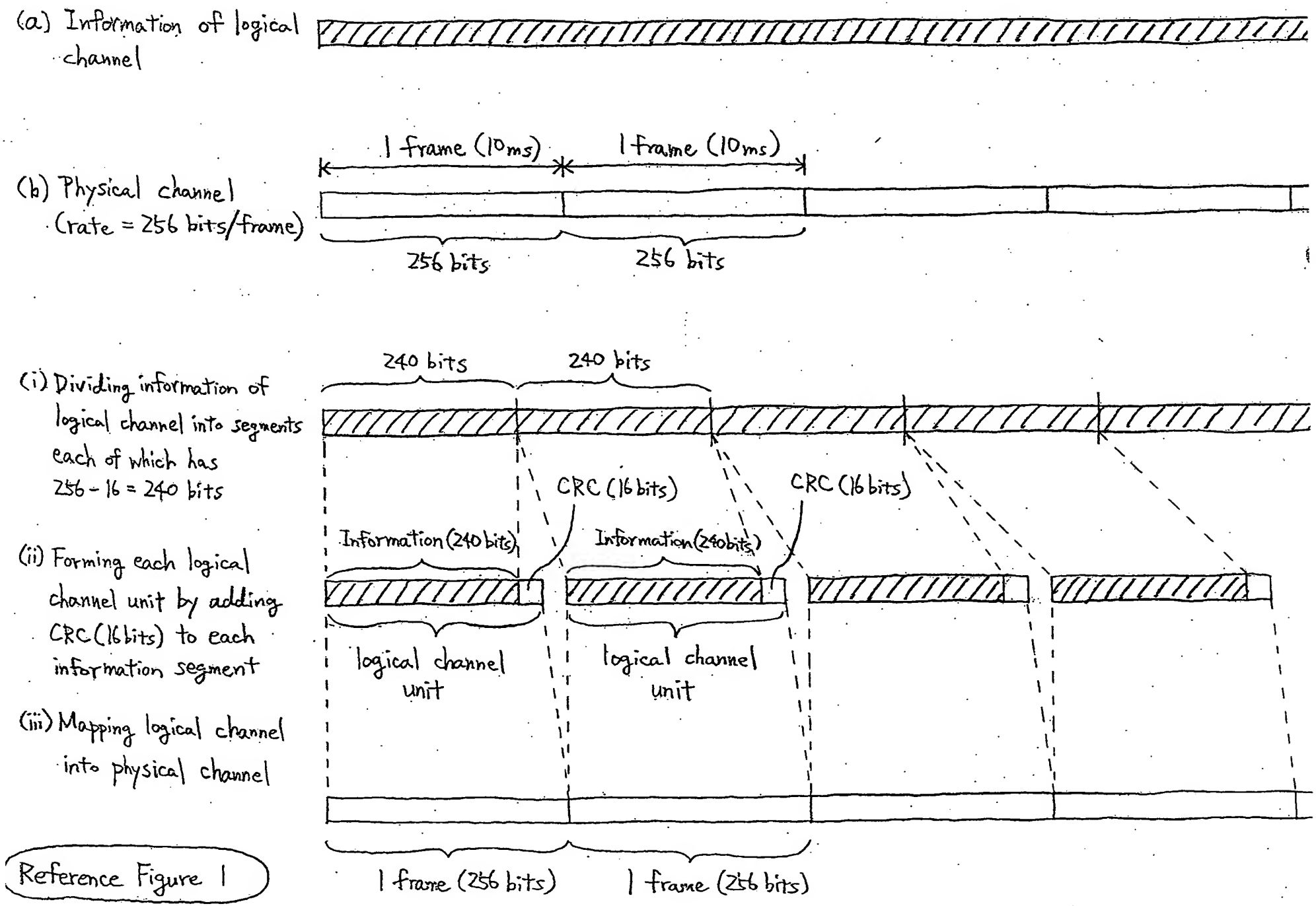
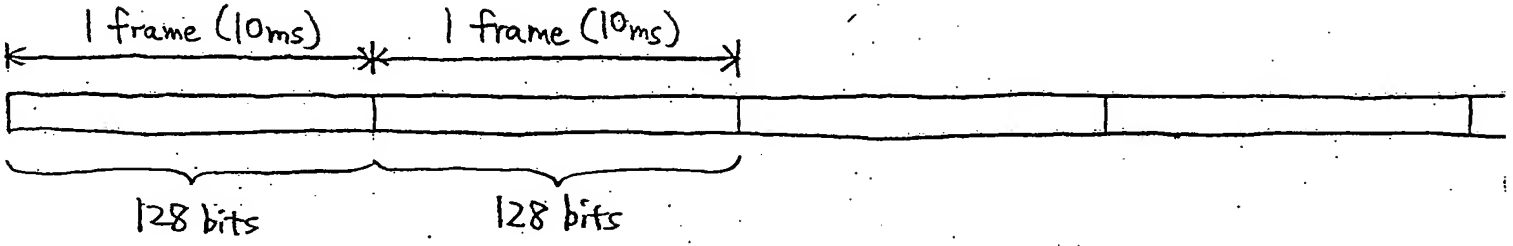
APPENDIX



Reference Figure 1

(a) Information of logical channel

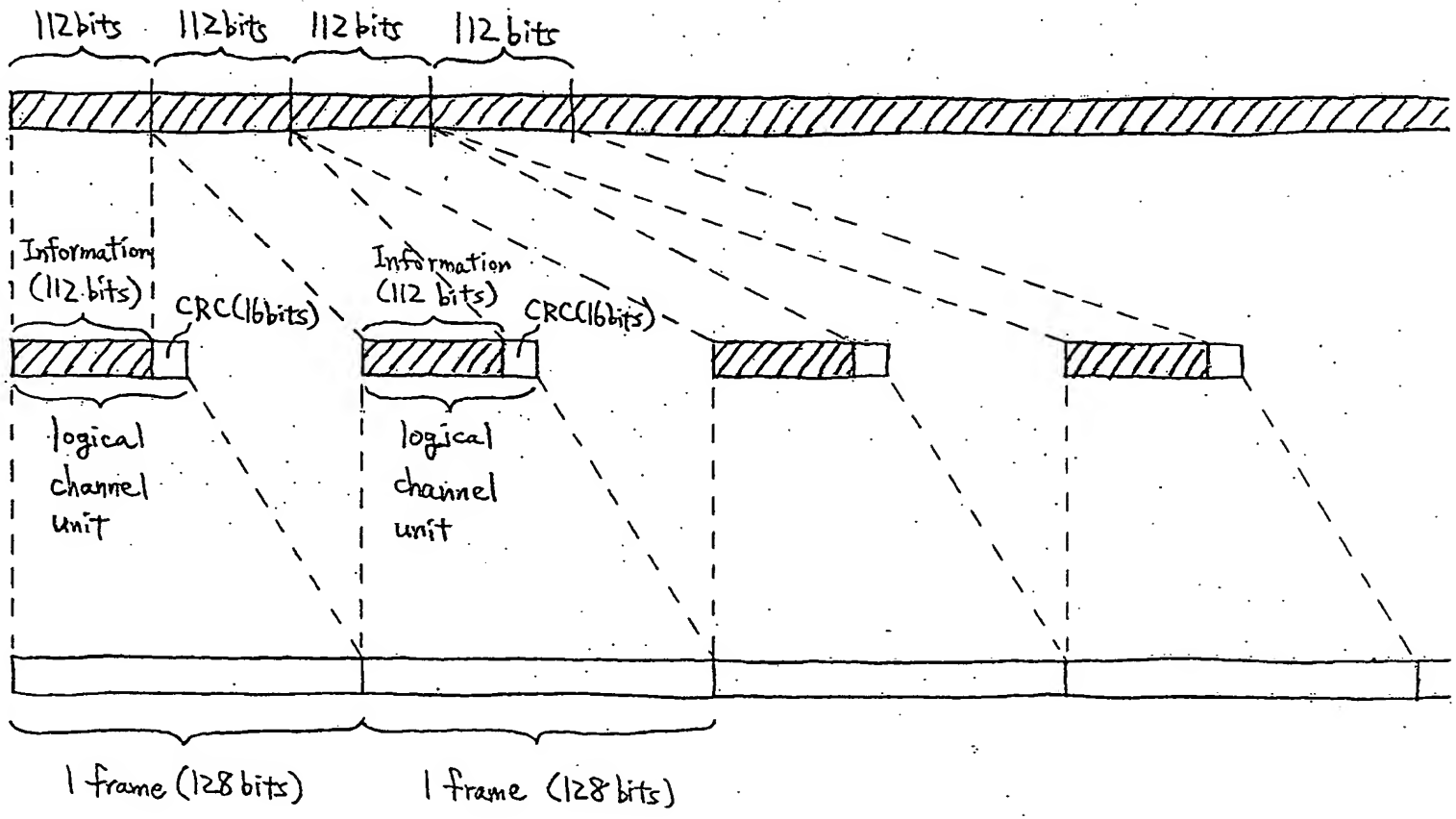
(b) Physical channel (rate = 128 bits/frame)



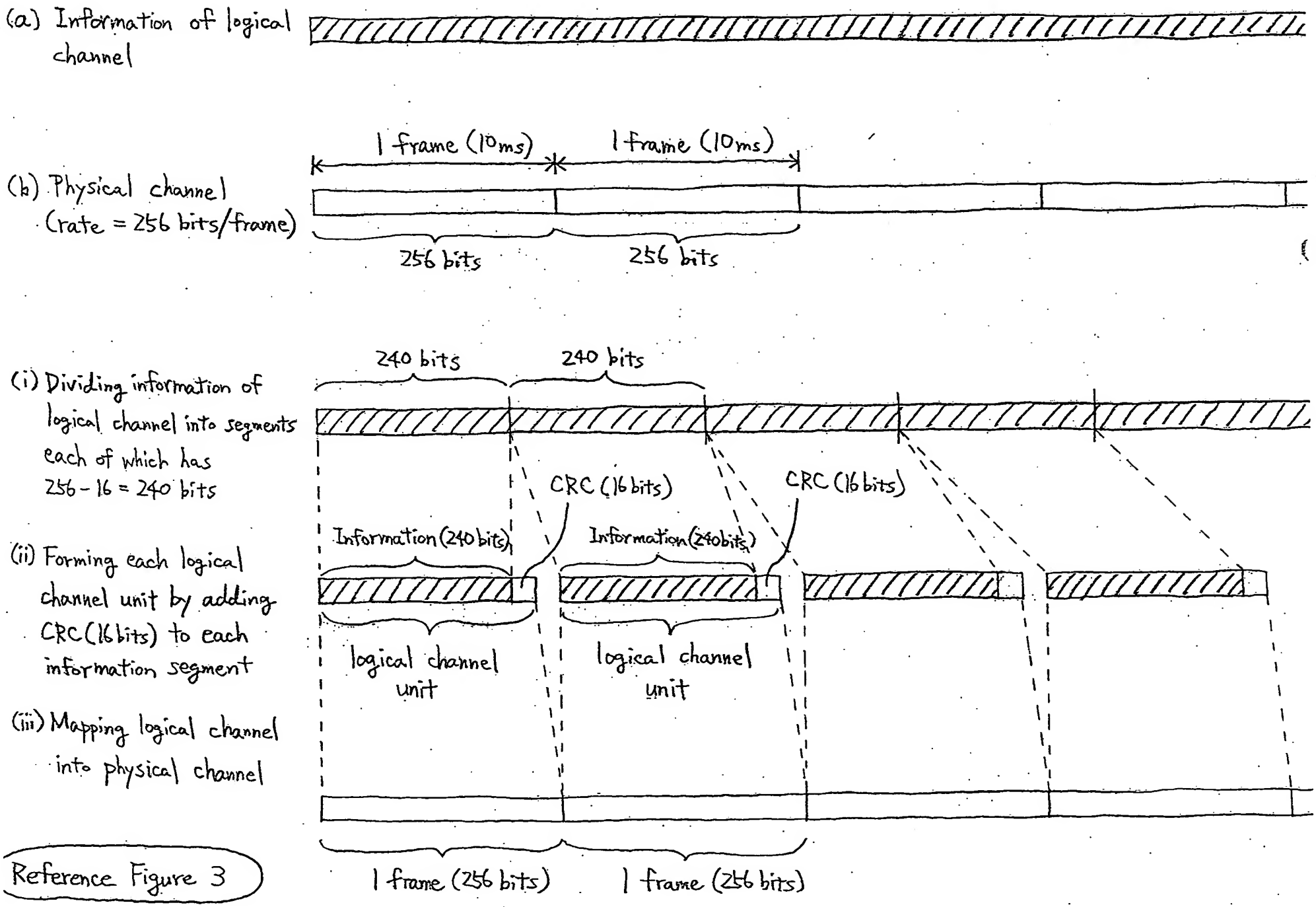
(i) Dividing information of logical channel into segments each of which has 128 − 16 = 112 bits

(ii) Forming each logical channel unit by adding CRC (16bits) to each information segment

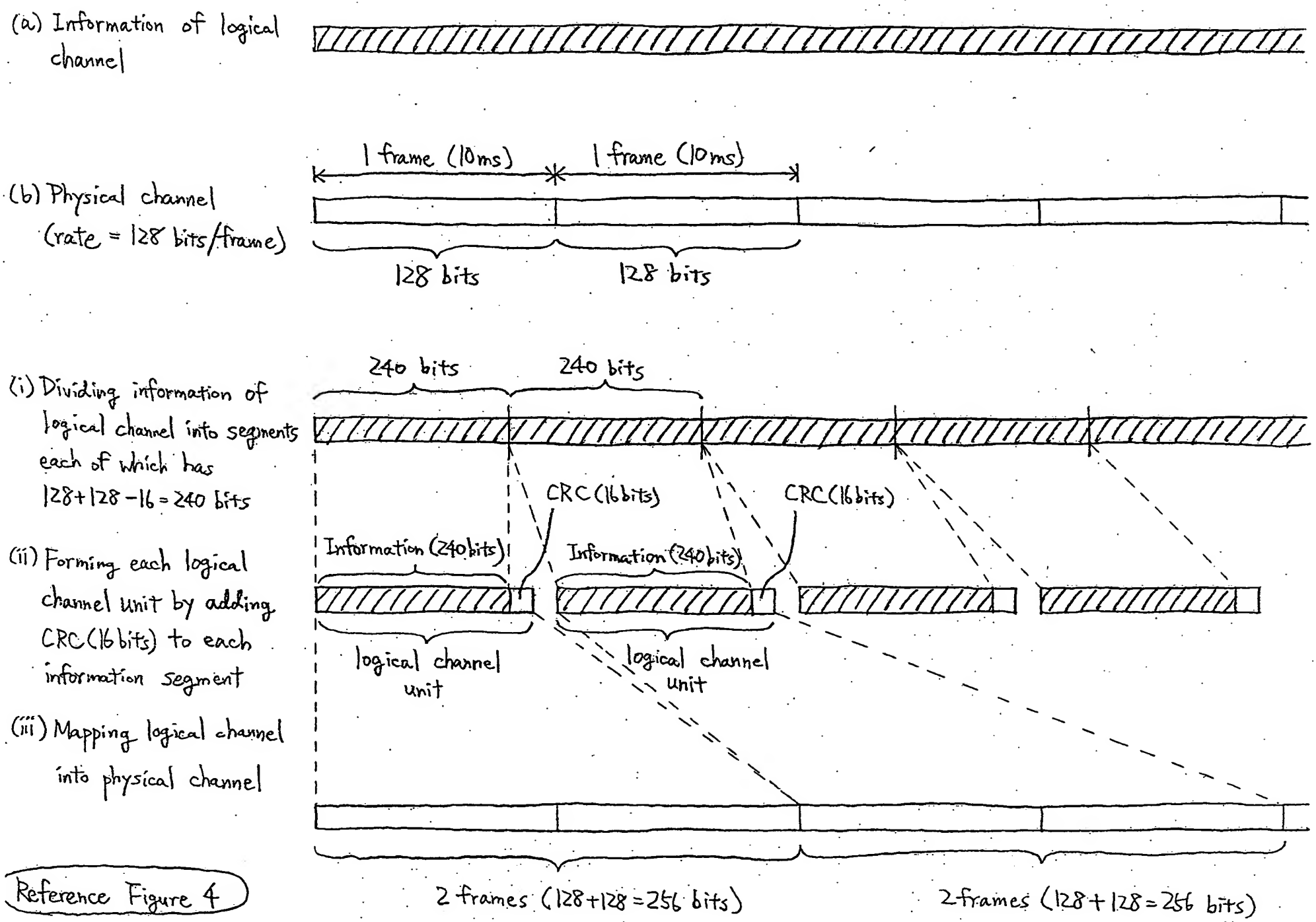
(iii) Mapping logical channel into physical channel



Reference Figure 2



Reference Figure 3



Reference Figure 4